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**\*\* CONTINUING DATA \*\*\*\*\***

This application is a DIV of 10/196,229 07/17/2002 PAT 6,642,564

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***

JAPAN 2001-218277 07/18/2001

**IF REQUIRED, FOREIGN FILING LICENSE GRANTED**

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Foreign Priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature _____ Initials _____				

**ADDRESS**

20277

**TITLE**

SEMICONDUCTOR MEMORY AND METHOD FOR FABRICATING THE SAME

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